

A linear functional first-order intermediate language for verified compilers

Sigurd Schneider, Sebastian Hack, Gert Smolka

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Introduction

Binding vs. assignment

Binding

Assignment

Binding

Assignment

let $x=e$ in s

- x is bound in term s
- *functional*

Binding

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Assignment

$x := e; s$

- x is a register
- *imperative*

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SSA-based register assignment
Translation from binding to assignment

Intermediate language IL

A linear first-order functional language with external calls

$s, t ::=$	term
let $x = e$ in s	variable binding
let $x = \alpha$ in s	external call
if e then s else t	conditional
e	value
fun $f \bar{x} = s$ in t	function definition
$f \bar{e}$	application

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Functions f, g not first-class

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3 Linear simpl.

Restricted sequentialization

let $x = e$ in s (not: $s; t$)

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let $x = \alpha$ in s

Example

A functional and an imperative interpretation

$$F(n, m) := n * (n + 1) * \dots * m$$

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Functional IL

```
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Imperative IL/I

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```

No closure
created:
goto

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```

Parameter passing
in IL/I is parallel
assignment:
 $j, p := k, m$

Example

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Imperative IL/I

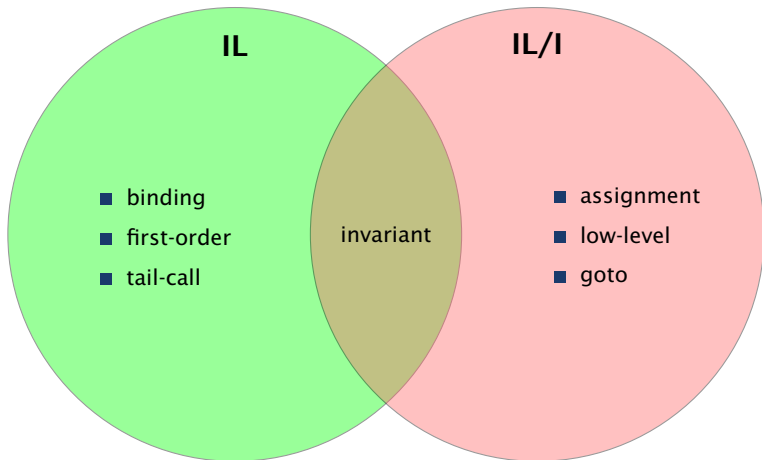
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```

- When renamed-apart, binding and assignment interchangeable!

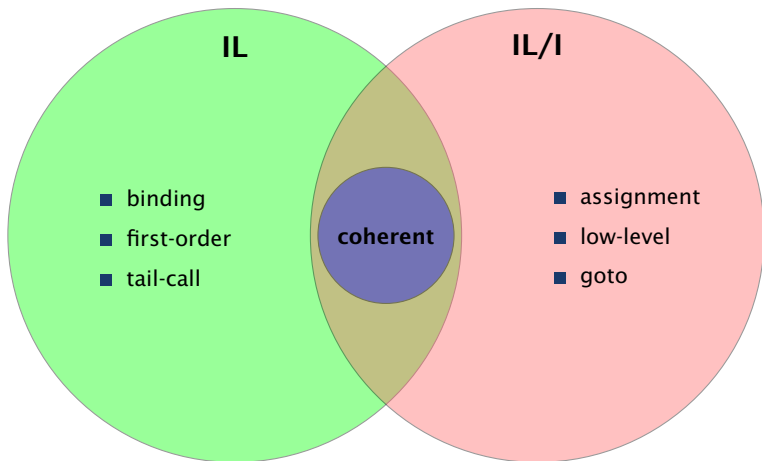
Overview

Translating from the functional to the imperative interpretation



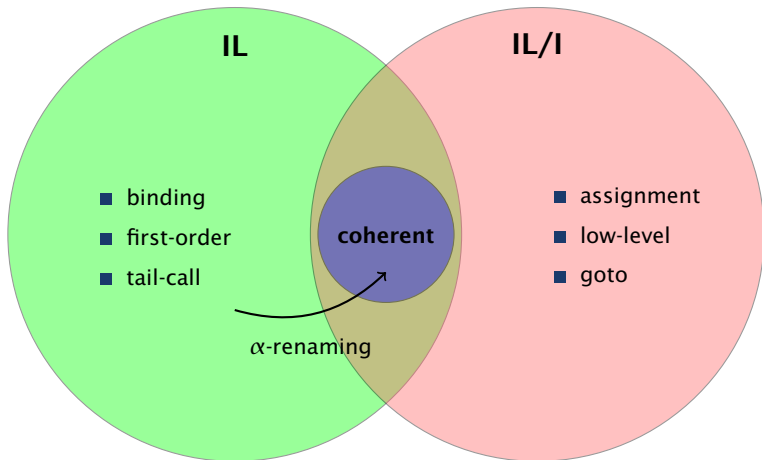
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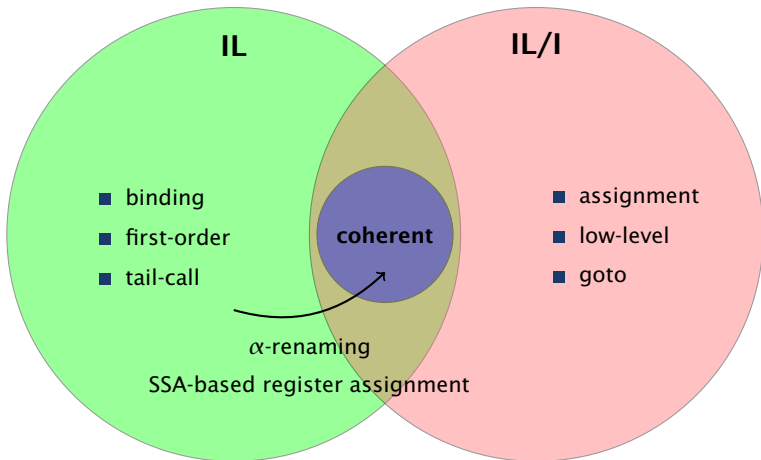
Overview

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Overview

Translating from the functional to the imperative interpretation



Related work

Static single assignment (SSA)

Related work

IL

```

1 let i = 1 in
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3
4   let c = p <= m in
5   if c then
6     let k = p * j in
7     let x = p + 1 in
8     f (k,x)
9   else
10    j
11 in f (i,n)
  
```

SSA

```

1   i := 1;
2  f:
3   j := φ(i,k), p := φ(n,x)
4   c := n <= m;
5   if c then
6     k := p * j;
7     x := p + 1;
8     goto f
9   else
10    return i
  
```

- SSA \longleftrightarrow CPS due to Appel (1998) and Kelsey (1995).
- Chakravarty et al. (2003) reformulates SSA optimization on a functional language in ANF (Sabry et al. 1993).
- IL is a sub-language (up to system calls)

SSA in verified compilers

Related work

- 1 **CompCertSSA: Barthe et al. (2012)**
 - ▶ Integrates SSA-based optimization passes in CompCert (Leroy (2009))
 - 2 **VeLLVM: Zhao et al. (2012)**
 - ▶ Verifies some SSA-based passes of LLVM
-
- **SSA for optimizations**
 - ▶ performance of data-flow analyses
 - **ϕ -functions**
 - ▶ no functional language
 - ▶ underlying semantics uses imperative variables

SSA-based register allocation

Related work

- SSA-based register allocation (Hack et al. (2006))
 - ▶ allows phase separation of spilling and register assignment
 - ▶ IL version similar to Appel (1992)
 - ▶ not considered in verified setting so far:
out of SSA + non-SSA register allocation
- Blazy et al. (2010) verify non-SSA register allocation algorithm (which must include spilling)
- We only considering register assignment, because SSA-based algorithm allows spilling to be separate phase

Functional and imperative semantics

Related work

- Beringer et al. (2003) use a language with a functional and imperative interpretation for proof-carrying code.
- Grail normal form (GNF) sufficient for functional + imperative semantics to coincide
- Main difference: GNF requires functions to be closure converted, i.e. all variables a function body depends on must be parameters

- Coherence
 - ▶ relates binding and assignment directly
 - ▶ another perspective on SSA and functional programming
- SSA-based register assignment on IL
 - ▶ formal correctness proof (using coherence)
 - ▶ key property from SSA holds on IL:
spilling can be considered separately (not possible without SSA)
- Coq development available online:
www.ps.uni-saarland.de/~sdschn/publications/lvc15

Semantics and program equivalence

Semantics of IL and IL/I

Reduction, events, configurations

- Small-step relation $\xrightarrow{\phi}$
- Decorated with events ϕ

$\phi ::= \tau$	silent event
$v = \alpha$	external event

- Configurations

IL: (F, V, s)

IL/I: (L, V, s)

- ▶ F function env. (with closures)
- ▶ L block env. (no closures)
- ▶ V variable env.
- ▶ s program

Program equivalence

Non-determinism and equivalence

$$\begin{array}{c}
 \text{EXTERN} \\
 \frac{v \in \mathbb{V}}{F \mid V \quad | \text{let } x = \alpha \text{ in } s} \\
 \xrightarrow{v = \alpha} F \mid V[x \mapsto v] \mid s
 \end{array}$$

- $\xrightarrow{\phi}$ forms a LTS
- Internally deterministic reduction systems (IDRS)
 - ▶ $\sigma \xrightarrow{\phi} \sigma_1 \wedge \sigma \xrightarrow{\tau} \sigma_2 \Rightarrow \phi = \tau$ τ-deterministic
 - ▶ $\sigma \xrightarrow{\phi} \sigma_1 \wedge \sigma \xrightarrow{\phi} \sigma_2 \Rightarrow \sigma_1 = \sigma_2$ action-deterministic
- Configurations are equivalent (\simeq), if they allow the same partial traces

$$\pi ::= \epsilon \mid v \mid \perp \mid v = \alpha, \pi$$

- Sound and complete characterization via (stutter) bisimulation

Liveness

Liveness

Judgment

$$(L, \mathbf{V}, s) \stackrel{?}{\simeq} (L, \mathbf{W}, s)$$

$$V =_x W \Rightarrow (L, V, s) \stackrel{?}{\simeq} (L, W, s)$$

$$V =_X W \Rightarrow (L, V, s) \stackrel{?}{\simeq} (L, W, s)$$

$\Lambda \vdash \mathbf{live} s : X$

Λ live variables of functions
 s program
 X set of live variables

- embedded liveness analysis results as annotations in syntax:

$\text{fun } f \bar{x} : X_1 = s_1 \text{ in } s_2$

- syntactic structure allows for inductive specification
- useful for imperative IL/I
- judgment monotonic in X (larger sets are sound)

Liveness

Properties for IL/I

Theorem (Decidability)

$\Lambda \vdash \mathbf{live} s : X$ *decidable*.

Theorem (Soundness)

If

1 $\Lambda \vdash \mathbf{live} s : X$

2 $L \models \Lambda$

3 $V =_X W$

liveness information sound

Λ *sound for blocks* L

V, W *agree on live set* X

then

$$(L, V, s) \simeq (L, W, s)$$

Coherence

f available as long as no global rebound

Coherence

Inductive definition

f available as long as no global rebound

not invariant

```
1 let x = 7 in  
2 fun f () : {x} = x in  
3 let x = 5 in  
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f unavailable after line 3

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coherent

```
1 let x = 7 in
2 fun f () : {x} = x in
3 let y = 5 in
4 f ()
```

f available in line 4

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f available in line 4

Coherence judgment $\Lambda \vdash \mathbf{coh} s$

- ensures s only applies available functions
- defined relative to liveness information

$\Lambda - \{x\}$ removes definitions from Λ that require x as global

$$\text{COH-OP} \frac{\Lambda - \{x\} \vdash \mathbf{coh} \ s}{\Lambda \vdash \mathbf{coh} \ \text{let } x = e \text{ in } s}$$

$$\text{COH-APP} \frac{\Lambda f \neq \perp}{\Lambda \vdash \mathbf{coh} \ f \bar{y}}$$

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$[\Lambda]_X$ removes definitions from Λ that require more globals than X

$$\text{COH-FUN} \frac{\Lambda; f : X \vdash \mathbf{coh} \ t \quad [\Lambda; f : X]_X \vdash \mathbf{coh} \ s}{\Lambda \vdash \mathbf{coh} \ \text{fun } f \bar{x} : X = s \text{ in } t}$$

Coherence

Results

We define $strip(V, \bar{x}, s) = (\bar{x}, s)$ and lift *strip* pointwise to contexts.

Theorem (Coherence implies invariance)

If

- | | | |
|---|--|--|
| 1 | $\Lambda \vdash \mathbf{coh} s$ | <i>s is coherent</i> |
| 2 | $\Lambda \vdash \mathbf{coh} F$ | <i>definitions in F are coherent</i> |
| 3 | $\Lambda' \vdash \mathbf{live} s : X$ for $\Lambda \preceq \Lambda'$ | <i>liveness information is sound</i> |
| 4 | $V =_X W$ | <i>V, W agree on X</i> |
| 5 | $F, V \vDash \Lambda$ | <i>closures in F agree with V on globals</i> |

then

$$(F, V, s)_F \simeq (strip F, W, s)_I$$

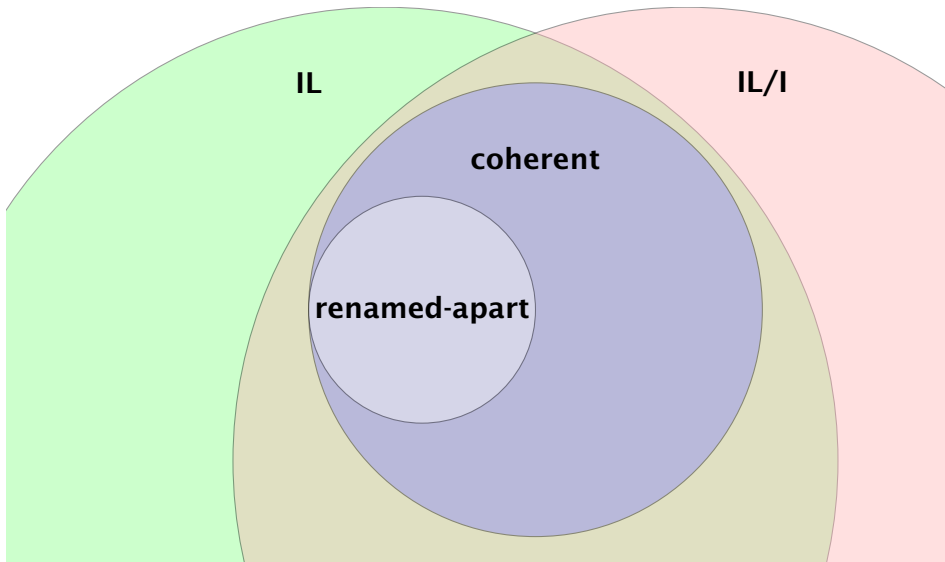
Register assignment

Register assignment

- State-of-the-art SSA-based register assignment algorithm
 - ▶ decouples spilling from assignment:
 - number of registers bounded by largest live set
 - ▶ polynomial-time (coalescing is NP-hard)
 - ▶ critically depends on dominance ordering
- Register assignment for functional language IL
 - ▶ same properties: register bound, polynomial time
 - ▶ straight-forward recursion on syntax
- Correctness argument of assignment phase
 - ▶ does not involve dominance
 - ▶ via coherence and α -equivalence

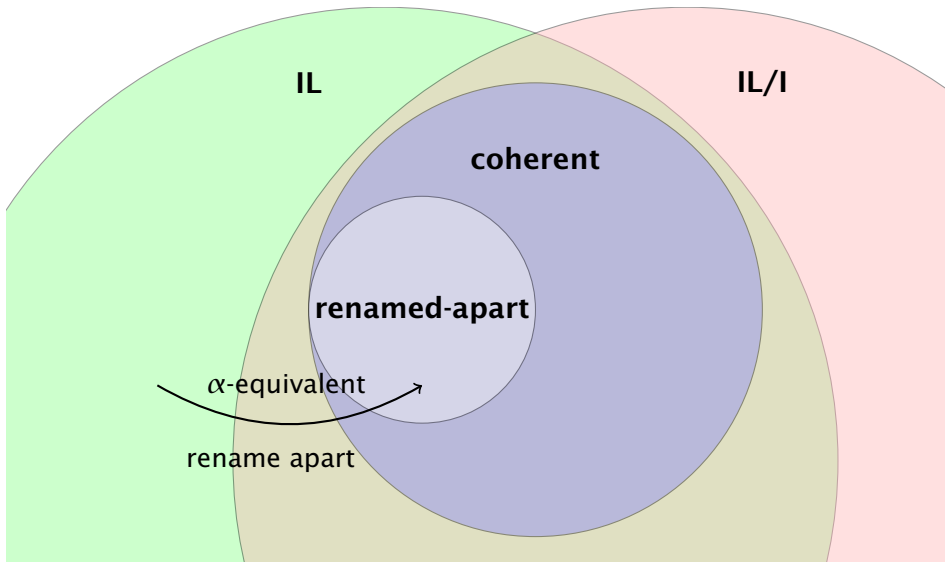
Register assignment

Proof overview



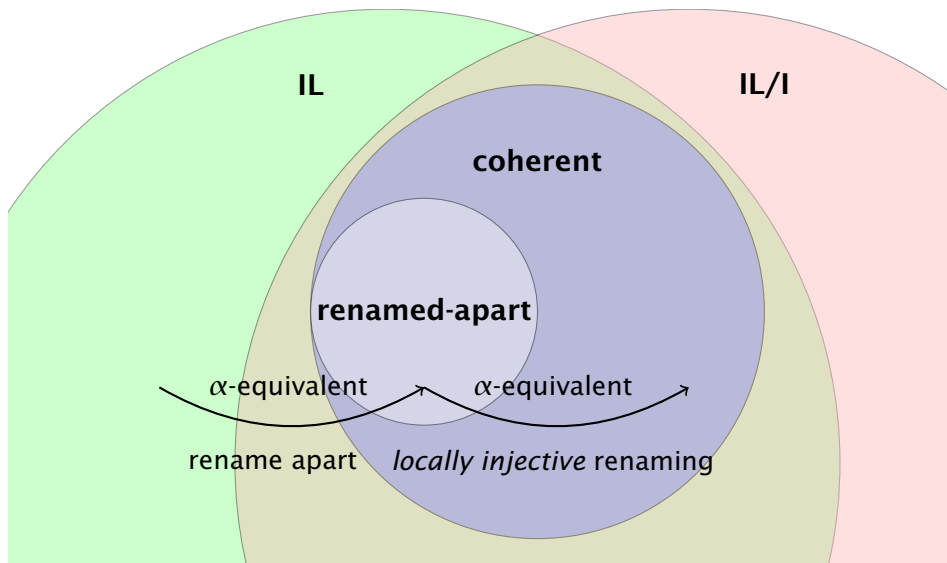
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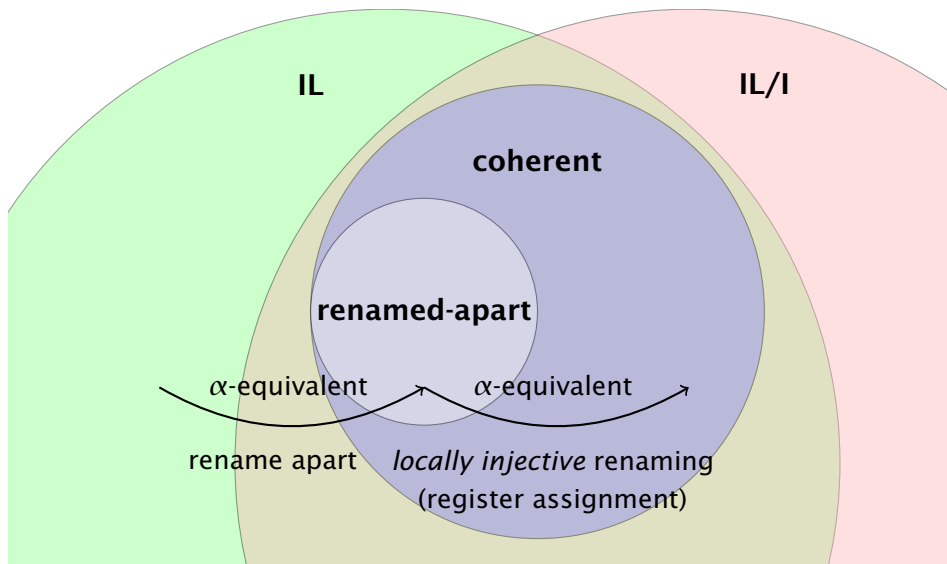
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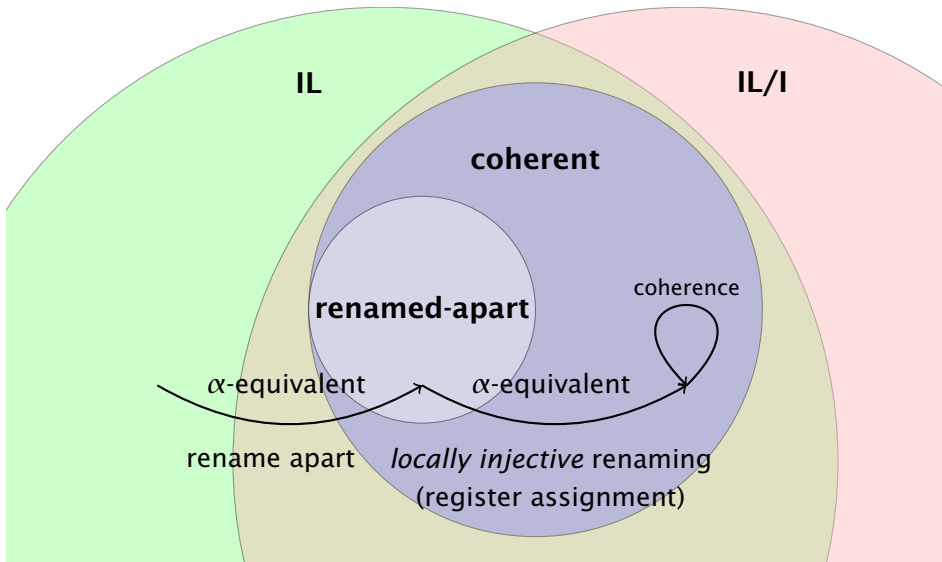
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Overview and example

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1 Rename apart

- ▶ Every assignment can be represented as $\rho : \mathcal{V} \rightarrow \mathcal{V}$

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2 Rename with *locally injective* ρ

A ρs is α -equivalent and coherent

B register assignment algorithm yields locally injective renaming

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3 Reinterpret binding as assignment: IL/I

- Call Λ and s *suitable* if
 - 1 s renamed-apart
 - 2 $\Lambda \vdash \mathbf{live} s : X$
 - * write $[s]$ for X

liveness sound

■ Call Λ and s *suitable* if

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Local Injectivity

Local Injectivity $\boxed{\rho \vdash \mathit{inj} s}$ requires $\rho : \mathcal{V} \rightarrow \mathcal{V}$ to be injective on every live set X that appears in the liveness derivation.

ρ^{-X} : inverse of ρ on X ,

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Local Injectivity $\boxed{\rho \vdash \mathit{inj} s}$ requires $\rho : \mathcal{V} \rightarrow \mathcal{V}$ to be injective on every live set X that appears in the liveness derivation.

Theorem (A)

If

- 1 Λ and s suitable
- 2 s without unreachable code
- 3 $\rho \vdash \mathit{inj} s$

ρ locally injective

then

- 1 $\rho([\Lambda]_{[s]}) \vdash \mathbf{coh}(\rho s)$ ρs coherent
- 2 ρ injective on $\text{fv}(s) \implies \rho, \rho^{-\text{fv}(s)} \vdash \rho s \sim_{\alpha} s$ ρs α -equivalent to s

ρ^{-X} : inverse of ρ on X ,

Register assignment algorithm

Definition

- Assume $\text{fresh} : \text{set } \mathcal{V} \rightarrow \mathcal{V}$ such that $\text{fresh } X \notin X$ for all finite X .
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 - separation of concerns: correctness and code quality
- rassign yields renaming $\mathcal{V} \rightarrow \mathcal{V}$

$$\text{rassign } \rho \text{ (let } x = e \text{ in } s) = \text{rassign } (\rho[x \mapsto y]) s$$

where $y = \text{fresh } (\rho([s] \setminus \{x\}))$

$$\text{rassign } \rho \text{ (if } e \text{ then } s \text{ else } t) = \text{rassign } (\text{rassign } \rho s) t$$

$$\text{rassign } \rho e = \rho$$

$$\text{rassign } \rho (f \bar{e}) = \rho$$

$$\text{rassign } \rho \text{ (fun } f \bar{x} : X' = s \text{ in } t) = \text{rassign } (\text{rassign } (\rho[\bar{x} \mapsto \bar{y}]) s) t$$

where $\bar{y} = \text{freshlist } (\rho([s] \setminus \bar{x})) |\bar{x}|$

rassign recurses on program structure,
 while SSA algorithm must process statements in dominance order

Register assignment algorithm

Correctness and bound on registers

Theorem (B)

Let Λ and s suitable and ρ injective on $[s]$. Then: $\text{rassign } \rho \ s \vdash \mathbf{inj} \ s$.

Assume variables totally ordered: $x_0 < x_1 < x_2 \dots$

Theorem (Register Bound)

If

- 1 Λ and s suitable
- 2 \forall finite sets of variables Y : $\text{fresh } Y \in \{x_0, \dots, x_{|Y|}\}$
- 3 k is size of largest set of live variables in s
- 4 $\rho(\text{fv}(s)) \subseteq \{x_0, \dots, x_n\}$.
- 5 $\rho' = \text{rassign } \rho \ s$

Then $\rho' (\mathcal{V}_O(s)) \subseteq \{x_0, \dots, x_{\max\{n, k\}}\}$

- This work is part of a very simple verified compiler
- Extraction yields binary that handles the running example
 - ▶ Efficient finite set library with type classes (Lescuyer 2012)
 - ▶ Cannot assume set extensionality
 - ▶ Decision procedures for equivalence on many types
- Development almost completely constructive
 - ▶ UIP required for Paco Library (Hur et al. (2013))
- Formal development contains proofs of
 - ▶ Backwards translation: IL/I to IL (SSA-construction)
 - ▶ Dead code elimination
 - ▶ Sparse conditional constant propagation
 - ▶ Translation validation for analysis results

- Coherence relates binding to assignment
- Correctness proof of register assignment on IL
 - ▶ same advantages as SSA (register bound)
 - ▶ correctness via coherence and α -equivalence
 - ▶ structural recursion instead of dominance ordering
- Coq development is available online¹

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Thanks! Questions?

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Semantics of IL and IL/I

Common rules

$\phi ::= \tau \mid v = \alpha$ events

$\xrightarrow{\phi}$ small step relation

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(F, V, s) F function env.
 V variable env.
 s program

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OP

$$\frac{\llbracket e \rrbracket V = v}{\xrightarrow{\tau} F \mid V \mid \text{let } x = e \text{ in } s} \quad F \mid V \mid \text{let } x = e \text{ in } s$$

EXTERN

$$\frac{v \in \mathbb{V}}{\xrightarrow{v=\alpha} F \mid V \mid \text{let } x = \alpha \text{ in } s} \quad F \mid V \mid \text{let } x = \alpha \text{ in } s$$

COND

$$\frac{\llbracket e \rrbracket V = v \quad \beta(v) = i}{\xrightarrow{\tau} F \mid V \mid \text{if } e \text{ then } s_0 \text{ else } s_1} \quad F \mid V \mid \text{if } e \text{ then } s_0 \text{ else } s_1$$

IL

FUN

$$\frac{F}{\xrightarrow{\tau} F; f : (V, \bar{x}, s) \mid V \mid t} \mid V \mid \text{fun } f \bar{x} = s \text{ in } t$$

APP

$$\frac{[[\bar{e}]] V = \bar{v} \quad Ff = (W, \bar{x}, s)}{\xrightarrow{\tau} Ff \mid W[\bar{x} \mapsto \bar{v}] \mid s} F \mid V \mid f \bar{e}}$$

Semantics IL and IL/I

Differences

IL

FUN

$$\frac{F \quad |V| \text{fun } f \bar{x} = \text{sin } t}{\xrightarrow{\tau} F; f : (V, \bar{x}, s) \mid V \mid t}$$

APP

$$\frac{[[\bar{e}]] V = \bar{v} \quad Ff = (W, \bar{x}, s)}{\xrightarrow{\tau} F \mid V \quad | f \bar{e}}$$

$$\xrightarrow{\tau} Ff \mid W[\bar{x} \mapsto \bar{v}] \mid s$$

IL/I

I-FUN

$$\frac{L \quad |V| \text{fun } f \bar{x} = \text{sin } t}{\xrightarrow{\tau}_I L; f : (\bar{x}, s) \mid V \mid t}$$

I-APP

$$\frac{[[\bar{e}]] V = \bar{v} \quad Lf = (\bar{x}, s)}{\xrightarrow{\tau}_I L \mid V \quad | f \bar{e}}$$

$$\xrightarrow{\tau}_I Lf \mid V[\bar{x} \mapsto \bar{v}] \mid s$$

Program equivalence

Internally deterministic reduction systems

Definition

A *reduction system* (RS) is a tuple $(\Sigma, \mathcal{E}, \longrightarrow, \tau, res)$, s.t.

1 $(\Sigma, \mathcal{E}, \longrightarrow)$ is a LTS

3 $res : \Sigma \rightarrow \mathbb{V}_\perp$

2 $\tau \in \mathcal{E}$

4 $res \sigma = v \Rightarrow \sigma \longrightarrow$ -terminal

An *internally deterministic* reduction system (IDRS) additionally satisfies

5 $\sigma \xrightarrow{\phi} \sigma_1 \wedge \sigma \xrightarrow{\phi} \sigma_2 \Rightarrow \sigma_1 = \sigma_2$

action-deterministic

6 $\sigma \xrightarrow{\phi} \sigma_1 \wedge \sigma \xrightarrow{\tau} \sigma_2 \Rightarrow \phi = \tau$

τ -deterministic

Program equivalence

Trace equivalence

$$\Pi \ni \pi ::= \epsilon \mid \nu \mid \perp \mid \phi\pi$$

$$\sigma \triangleright \pi$$

$$\phi \neq \tau$$

partial trace

σ produces π

Program equivalence

Trace equivalence

$$\Pi \ni \pi ::= \epsilon \mid \nu \mid \perp \mid \phi\pi$$

$$\sigma \triangleright \pi$$

$$\phi \neq \tau$$

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σ produces π

Definition (Trace equivalence)

$$\sigma \simeq \sigma' :\iff \forall \pi, \sigma \triangleright \pi \iff \sigma' \triangleright \pi$$

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$$\sigma \sim \sigma'$$

bisimilarity

Program equivalence

Trace equivalence

$\Pi \ni \pi ::= \epsilon \mid \nu \mid \perp \mid \phi\pi$ $\phi \neq \tau$ partial trace
 $\sigma \triangleright \pi$ σ produces π

Definition (Trace equivalence)

$\sigma \simeq \sigma' :\Leftrightarrow \forall \pi, \sigma \triangleright \pi \Leftrightarrow \sigma' \triangleright \pi$

$\sigma \sim \sigma'$ bisimilarity

Theorem (Soundness and completeness)

Let $(S, \mathcal{E}, \rightarrow, \text{res}, \tau)$ be an IDRS and $\sigma, \sigma' \in S$. Then:

$$\sigma \sim \sigma' \Leftrightarrow \sigma \simeq \sigma'$$

Local Injectivity

The problem with unreachable code

```
1 fun f () = x in  
2 y
```

```
1 fun f () = y in  
2 y
```

- $\{x \mapsto y, y \mapsto y\}$ locally injective
- Programs not α -equivalent