

Saarland University Faculty of Natural Sciences and Technology I Department of Computer Science Master's Program in Computer Science

Master's thesis

Permutation semantics of separation logic

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Statement

Hereby I confirm that thesis is my own work and that I have documented all sources used

Saarbruecken, December 01, 2006

Declaration of Consent

Hereby I agree that my thesis will be made available through the library of the Computer Science Department.

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Abstract

Separation logic is a recent extension of Hoare logic, developed by O'Hearn, Reynolds and others, where the separation conjunction p * q describes a disjoint partition of the heap. With this connective, a frame rule can be stated that facilitates local reasoning about programs using shared mutable data structures in the heap.

Originally, soundness of the logic was proved with respect to a non-deterministic memory allocation model. However, in some contexts, this "artificial" non-determinism interferes with other language features. I develop an alternative semantics of separation logic, based on deterministic memory allocation.

Contents

Ał	ostrac	t	iii
Ac	knov	vledgments	ix
1	Intro	oduction	1
	1.1	Separation Logic	1
	1.2	Description of the Problem	2
	1.3	Permutation semantics	2
2	Lang	guage and Semantics	5
	2.1	Language Syntax	5
	2.2	Domains	6
	2.3	Language Semantics	6
3	Pern	nutation	9
	3.1	Definition	9
	3.2	Permutation Theorem	10
4	Logi	ic	17
	4.1	Assertions	17
	4.2	Partial Correctness	18
	4.3	Inference rules	19
5	Sou	ndness	23
6	Con	clusion and Future Work	29
	6.1	Conclusion	29
	6.2	Future Work	29
Bi	bliog	raphy	31

List of Tables

2.1	Language syntax	5
2.2	Language semantics: Booleans	6
2.3	Language semantics: Expressions	7
2.4	Language semantics: While Commands	7
2.5	Language semantics: Heap Commands	8
4.1	Assertions	17
4.2	Inference rules	19

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Chapter 1

Introduction

1.1 Separation Logic

In the end of the 60-s Hoare developed a logic for reasoning about the correctness of the programs written in a simple imperative *while*-language[4]. Since then the logic has been applied by many researchers to various more complex languages, like: machine code[6], object-oriented, [2], [14], procedural [10] languages. Central point in this logic is a notion of *Hoare triple*, $\{p\}c\{q\}$, which describes the state change induced by a piece of a program - running a command *c* in a state, where assertion *p* is true, results in a state, where assertion *q* holds. Command *c* is of *while*-language and it changes (unless it is **skip**) the *state* of the program, represented by *Store*, which is a mapping of variables to values, and described by assertions.

Separation logic is an extension of Hoare logic for reasoning about programs that use shared mutable data structures, usually called pointer programs. It was developed by joint efforts of John Reynolds[12], Peter O'Hearn, Hongseok Yang[8] and others in the first years of this century based on early ideas of Burstall [3] and Pym and O'Hearn's logic of Bunched Implications, [7]. Program state in programming language with heap operations is represented now not only by *Store* but by pair *Store* x *Heap*, and new commands and assertions were added to reflect these changes. Most importantly *- separating conjunction*: *, which asserts that its subformulas hold for disjoint parts of the heap. This connective solves the problem with aliasing -when a memory cell is pointed from different parts of a program, and its alteration affects all of them- appearing for example in a rule of constancy, replacing it with *Frame rule*. This rule is vital for *local reasoning* - possibility to concentrate only on those parts of the memory which are actually accessed by the procedure, as O'Hearn/Yang named it-the *footprint* [17], being sure that the rest part of the memory will remain unmodified.

The relatively young logic has already been successfully used by many researchers in practice: Yang's proof of the Schorr-Waite algorithm[16], Torp-Smith's proof of Cheney algorithm[13], various extensions of this logic by different authors for concurrency, higher-order languages, e.g. see [9], [5].

1.2 Description of the Problem

Having all these advantages nevertheless the usual semantics of the separation logic is not always suitable for extensions of the logic and in some cases shows to be inconvenient. One particular problem which we will try to solve in this work is about non-deterministic behaviour of memory allocation. Besides the requirement for the cells to be previously inactive and consequtive, the choice of locations is indeterminate. This problem was mentioned by several researchers:

"Previous work on modularity, simulation and refinement in separation logic has run into some technical difficulties associated with the non-deterministic treatment of allocation..."

Nick Benton[1]

"...in the presence of higher-order store where we have to solve recursive domain equations we found the use of (countable) non-determinism quite challenging (for instance, programs would no longer denote ω -continuous functions)."

Bernhard Reus, Jan Schwinghammer[11]

1.3 Permutation semantics

Yang/O'Hearn in their work "Semantic basis for local reasoning" [17] pointed out the reason for relying on a non-deterministic semantics and the way to solve this problem:

"Non-deterministic allocation is used to force a program proof not to depend on details of how the allocator might work (In a language without address arithmetic, we could use invariance under location renaming rather than non-determinism in allocation to ensure this sort of independence)"

and here we will implement these ideas and prove the logic to be still sound.

We will check for O'Hearn's instructions and first in a Chapter 2 we give a language without address arithmetic, then in Chapter 3 exploit renaming of locations, in Chapter 4 we provide the logic for our language and in the next chapter prove the soundness of this logic.

In order to restrict address arithmetic we step back to the earlier versions of separation logic, rather than use Reynolds' version, and divide *Values* into classes of *Integers*, *Atoms* and *Locations*.

We change non-deterministic memory allocation operator **cons** to deterministic operator **new**, which will allocate the minimal inactive cell. The other commands will remain unchanged.

Having such a language we define the operation of *permutation*, which is a bijective function from *Locations* to *Locations* and prove a **permutation theorem**:

Running a consistently renamed program on a correspondingly renamed heap leads to the same result, up to a renaming of locations in the final heap.

Permutation: $\pi : Loc \rightarrow Loc$

 $\langle c; \sigma \rangle \rightsquigarrow \sigma' \Rightarrow \exists \pi'. \langle c; \pi \bullet \sigma \rangle \rightsquigarrow \sigma'' \land \sigma' = \pi' \bullet \sigma''$

Chapter 2

Language and Semantics

As it was said in the introduction, we will follow the direction pointed by O'Hearn, and first of all we need a language without address arithmetic. For that reason we will step back from Reynolds' version of the separation logic[12] and treat *Integers*, *Locations* and *Atoms* as distinct kinds of *Values*.

2.1 Language Syntax

Programming language we use is the version of simple imperative while language extended to reflect changes in the state model (heap commands).

There are three syntactic categories: **booleans**, **expressions** and **commands**: Table 2.1. Commands are usual commands of *while*-language: **skip** - no operation, c_0 ; c_1 - sequencing, conditional, while loop and assignment; and the last four commands allow operations on the heap.

$x, y \in Variable$	s $[e] - contents of the heap at the low$	cation of e
b ::=	$false \mid e_0 \leq e_1 \mid \neg b \mid b_0 \lor b_1$	
<i>e</i> ::=	$x \mid n \mid e_0 + e_1 \mid e_0 - e_1 \mid e_0 \times e_1$	
c ::=	skip c_0 ; c_1 if b then c_0 else c_1	while b do $c \mid x := e$
	$x := \mathbf{new} e$	allocation
	$\mid x := [y]$	lookup
	$\mid [x] := e'$	mutation
	dispose e	deallocation

Table 2.1: Language syntax

6 Language and Semantics

2.2 Domains

Values = *Integers* ⊕ *Atoms* ⊕ *Locations* ⊕ *Booleans* Set of locations *Loc* isomorphic to *Naturals*

We make *Loc* isomorphic to *Naturals* because we will need an ordered set of locations with minimal element, to get our deterministic allocation.

Variables, countably infinite ranged over *x*, *y*...

Store is a function from variables to values. *Heap* is a function from finite set of locations to values. State is a pair of *Store* and *Heap*.

 $\begin{array}{l} \textit{Store} = \textit{Variables} \rightarrow \textit{Values} \\ \textit{Heap} = \bigcup_{\substack{A \subseteq \text{Locations}}} (A \rightarrow \textit{Values}) \end{array}$

State = Store \times Heap.

2.3 Language Semantics

Denotional semantics of the booleans and expressions is the same as in Hoare logic, but with arithmetic operations on non-integers (which are addresses) yielding **undefined**, to limit address arithmetic: Table 2.2, Table 2.3.

 $\llbracket e \rrbracket_s$ is the evaluation of expression *e* in a state with store *s*. Expressions depend only upon the *Store*.

$\llbracket b \rrbracket$: Store \rightarrow Booleans \cup { undefined }		
$\llbracket \mathbf{false} \rrbracket_s = false$		
$\llbracket b_0 \lor b_1 \rrbracket_s = \llbracket b_0 \rrbracket_s \lor \llbracket b_1 \rrbracket_s$		
$\llbracket \neg b \rrbracket_s = \neg \llbracket b \rrbracket_s$		
$[e_0 < e_s] = \int [e_0]_s \leq [e_1]_s,$	if both parts yield integer	
$\mathbb{L}^{e_0} \leq e_1 \mathbb{I}_s = \int undefined,$	otherwise	

Table 2.2: Language semantics: Booleans

Operational semantics of the original commands of the *while*-language usually considered by Hoare logic is slightly changed, since *State* is extended to contain *Heap*: Table 2.4. The triple $\langle c, s, h \rangle$, or pair $\langle s, h \rangle$ - where *c* is a command, s - store, h - heap, - is called *configuration*. There is a transition relation - \rightsquigarrow on configurations. Configurations can be:

```
\begin{split} \llbracket e \rrbracket : Stores &\to Values \cup \{ \texttt{undefined} \} \\ \llbracket x \rrbracket_s = s(x) \\ \llbracket n \rrbracket_s = n \\ \llbracket e_0 + e_1 \rrbracket_s &= \begin{cases} \llbracket e_0 \rrbracket_s + \llbracket e_1 \rrbracket_s, & \text{if both parts yield integer} \\ \texttt{undefined}, & \text{otherwise} \end{cases}, \text{same for } \times, - \end{split}
```

Table 2.3: Language semantics: Expressions

- 1. terminal, $\tau \equiv \langle s, h \rangle$ or **abort**
- 2. non-terminal: $\langle c, s, h \rangle$

Thus $\langle c, s, h \rangle \rightsquigarrow \langle s', h' \rangle$ reads as: "Running a command *c* from the state *s*, *h* terminates in the state *s'*, *h'*".

 $(f \mid x \mapsto y)$ means that the function f is the same, but with x mapping to y. Later we will use σ as an abbreviation of (s, h).

$\langle \mathbf{skip}, s, h \rangle \rightsquigarrow s, h$	
$\langle c_0, s, h \rangle \rightsquigarrow s'', h'' \qquad \langle c_1, s'', h'' \rangle \rightsquigarrow \tau$	$\langle c_0, s, h \rangle \rightsquigarrow \mathbf{abort}$
$\langle c_0; c_1, s, h angle \rightsquigarrow au$	$\langle c_0; c_1, s, h \rangle \rightsquigarrow \mathbf{abort}$
$\llbracket b \rrbracket_s = true \qquad \langle c_0, s, h \rangle \rightsquigarrow \tau$	$\llbracket b \rrbracket_s = false \qquad \langle c_1, s, h \rangle \rightsquigarrow \tau$
$\langle \mathbf{if} \ b \ \mathbf{then} \ c_0 \ \mathbf{else} \ c_1, s, h \rangle \rightsquigarrow \tau$	$\langle \mathbf{if} \ b \ \mathbf{then} \ c_0 \ \mathbf{else} \ c_1, s, h \rangle \rightsquigarrow \tau$
$\llbracket b \rrbracket_s = false$	
$\langle \mathbf{while} \ b \ \mathbf{do} \ c, s, h \rangle \rightsquigarrow s, h$	
$\llbracket b \rrbracket_s = true \qquad \langle c, s, h \rangle \rightsquigarrow s'', h''$	\langle while <i>b</i> do <i>c</i> , <i>s</i> ^{<i>''</i>} , <i>h</i> ^{<i>''</i>} $\rangle \rightsquigarrow s'$, <i>h</i> ^{<i>'</i>}
\langle while <i>b</i> do <i>c</i> , s	$\langle s,h angle \rightsquigarrow s',h'$
$\overline{\langle x := e, s, h \rangle} \rightsquigarrow s', h , s' = (s x \mapsto \llbracket e \rrbracket_s)$	

Table 2.4: Semantics of commands: While Commands

Semantics of the heap commands is given in the Table 2.5. Notation $h \cdot h'$, means the composition of two disjoint heaps h and h'.

Only one command of the *while* - allocation - is changed compared to Reynolds' version. Now it allows to allocate and initialize a single cell, which will be minimal inactive cell in the heap. Notice *minimal*, this makes the allocation deterministic. *lookup*

alloc	$\langle x := \mathbf{new} \ e, s, h \rangle \rightsquigarrow s', h'$, $s' = (s x \mapsto l)$, $h' = (h \cdot \llbracket e \rrbracket_s \rrbracket)$
		and $l = min(Loc - dom(h))$
lookup	$\frac{\llbracket e \rrbracket_s \notin dom(h)}{\langle x := [e], s, h \rangle \rightsquigarrow \text{abort}}$	
	$\frac{\llbracket e \rrbracket_s \in dom(h)}{\langle x := [e], s, h \rangle \rightsquigarrow s', h}$, $s' = (s x \mapsto h(\llbracket e \rrbracket_s))$
mutate	$\frac{\llbracket e \rrbracket_s \notin dom(h)}{\langle [e] := e', s, h \rangle \rightsquigarrow \mathbf{abort}}$	
	$\frac{\llbracket e \rrbracket_s \in dom(h)}{\langle [e] := e', s, h \rangle \rightsquigarrow s, h'}$, $h' = (h \llbracket e \rrbracket_s \mapsto \llbracket e' \rrbracket_s)$
dealloc	$\llbracket e \rrbracket_s \notin dom(h)$ (dispose $e, \sigma \rangle \rightsquigarrow abort$	
	$\frac{\llbracket e \rrbracket_s \in dom(h)}{\langle \mathbf{dispose} \; e, s, h \rangle \rightsquigarrow s, h'}$, $h = (h_0 \cdot \llbracket e \rrbracket_s \mapsto - \rrbracket)$ and $h' = h_0$

Table 2.5: Semantics of commands: Heap Operations

reads the value at the location of *e* and writes it to *x*. *mutate* updates the location *e* with value of *e'*. *dispose* deallocates location *e* from the heap.

All commands result in **abort** when trying to access inactive (not in the heap) location. Additionally any command aborts if boolean or expression used is **undefined**.

Chapter 3

Permutation

Now that we have a language with deterministic memory allocation it is time to introduce the location renaming. With allocation becoming deterministic we need another method to ensure that a program proof does not depend on details of allocator's work, and location renaming is such a method. Location renaming is the result of applying a permutation π , which is just a bijective function on *Loc*:

 $\pi: Loc \rightarrow Loc$

This function lifted to a value, store or a heap consistently renames all locations that appear inside. Having such a function is not enough yet, we need to prove *permutation theorem*, which says that "running a consistently renamed program on a correspondingly renamed heap leads to the same result, up to renaming of locations in the final heap". This means that it is always possible to find such a permutation, which when applied to the state resulting from running a command in a renamed(by another permutation) state would give us the original resulting state as if there was no permutation. This way we get the invariance under location renaming.

3.1 Definition

Permutation is a *bijective* function $\pi : Loc \rightarrow Loc.$: Since *Values* = *Integers* \biguplus *Locations* \biguplus *Atoms* \biguplus *Boolean* we can lift it to *Values, Stores* and *Heaps* as follows:

• $\pi^{val}: Values \rightarrow Values$

$$\pi^{val}(v) = \begin{cases} v & v \in Int \\ \pi(v) & v \in Loc \\ v & v \in Atom \\ v & v \in Boolean \end{cases}$$

As expected permutation on values has effect only when the value is a location.

- lift to π^s : Stores → Stores where Stores : Var → Values (π^s(s))(x) = (π^{val}(s(x))
- lift to π^h: Heaps → Heaps where Heaps : Loc → Values (π^h(h))(l) = (π^{val}(h(π^{-1 val}(l)))

We then define the action $\pi \bullet \sigma$ of a permutation π on a configuration σ by:

$$\pi \bullet \sigma = (\pi^s(s), \pi^h(h))$$

Later we will drop superscripts if clear from context or add them if needed.

3.2 **Permutation Theorem**

In this section we will prove permutation theorem, vital for having invariance under location renaming:

Theorem 3.1 (Permutation) For all c, σ, σ' and π holds:

- 1. $\langle c, \sigma \rangle \rightsquigarrow \sigma' \quad \Rightarrow \quad \exists \pi' . \langle c, \pi \bullet \sigma \rangle \rightsquigarrow \sigma'' \land \sigma' = \pi' \bullet \sigma''$
- 2. $\langle c, \sigma \rangle \rightsquigarrow \text{abort} \Rightarrow \langle c, \pi \bullet \sigma \rangle \rightsquigarrow \text{abort}$

Before proving this theorem we'll prove some useful lemmas.

Lemma 3.1 $\forall \pi, e, s. \llbracket e \rrbracket_s \neq \text{undefined} \Rightarrow \llbracket e \rrbracket_{\pi^s(s)} = \pi^{val}(\llbracket e \rrbracket_s)$

Proof: By structural induction on *e* we have:

3.
$$e = e_0 + e_1$$
 (cases for $-, \times$ analogously)
 $[\![e_0 + e_1]\!]_{\pi^s(s)} = [\![e_0]\!]_{\pi^s(s)} + [\![e_1]\!]_{\pi^s(s)}$ by Def. of $+$
 $= \pi^{val}([\![e_0]\!]_s) + \pi^{val}([\![e_1]\!]_s) = \pi^{val}([\![e_0 + e_1]\!]_s)$ by IH

Lemma 3.2

1. $\pi' \bullet (\pi \bullet \sigma) = (\pi' \circ \pi) \bullet \sigma$ 2. $id \bullet \sigma = \sigma$

Proof:

$$\begin{aligned} \pi' \bullet (\pi \bullet \sigma) &= \pi' \bullet (\pi^s(s), \pi^h(h)) & \text{by Def. of } \pi \bullet \sigma \\ &= (\pi'^s(\pi^s(s)), \pi'^h(\pi^h(h))) & \text{by Def. of } \pi \bullet \sigma \\ 1. &= ((\pi' \circ \pi)^s(s), (\pi' \circ \pi)^h)(h) & \text{Function composition} \\ &= (\pi' \circ \pi) \bullet \sigma & \text{by Def. of } \pi \bullet \sigma \\ \end{aligned}$$

2. *id* is an identity function , therefore so are id^{val} , id^s , id^h : $id \bullet \sigma = (id^s(s), id^h(h)) = s, h = \sigma$

Lemma 3.3 $dom(\pi^{h}(h)) = \{\pi(l) \mid l \in dom(h)\} = \{l' \mid \pi^{-1}(l) \in dom(h)\}$

Proof:

$$dom(\pi^{h}(h)) = \{l \in Loc \mid \pi(h(\pi^{-1}(l))) \text{ is defined} \} \quad by \ Def.$$

= $\{l \in Loc \mid h(\pi^{-1}(l)) \text{ is defined} \} \quad \pi \text{ total}$
= $\{l \in Loc \mid \pi^{-1}(l) \in dom(h)\}$ by Def.

Now we have everything to prove the theorem. We prove it by structural induction on derivations of $\langle c, \sigma \rangle \rightsquigarrow \sigma'$, walking through each command. For the first part we need to provide such a function π' , that when it is applied to the permutated state its result is the original state. In most cases such a function is simply inverse of original permutation. But the case with allocation operator is more tricky, since it allocates the minimal inactive cell in the heap and there is no guarantee that after permutation the minimal cell will remain to be the minimal.

In this part we consider only the cases which do not **abort**.

Part 1.

$$\langle c, \sigma \rangle \rightsquigarrow \sigma' \quad \Rightarrow \quad \exists \pi'. \langle c, \pi \bullet \sigma \rangle \rightsquigarrow \sigma'' \land \sigma' = \pi' \bullet \sigma''$$

Proof:

1. skip: $\langle \mathbf{skip}, \sigma \rangle \rightsquigarrow \sigma$ $\langle \mathbf{skip}, \pi \bullet \sigma \rangle \rightsquigarrow \pi \bullet \sigma$ Let $\pi' := \pi^{-1}$, then $\pi' \bullet \sigma'' = \pi' \bullet (\pi \bullet \sigma)$ by def. of σ'' $= (\pi' \circ \pi) \bullet \sigma$ by Lemma 2 $= (\pi^{-1} \circ \pi) \bullet \sigma$ by def. of π' = Id • σ by def. of Id by Lemma 2 $= \sigma$

2. new:

$$\begin{aligned} \langle x := \mathbf{new} \ e, s, h \rangle &\rightsquigarrow (s | x \mapsto l), (h * [l \mapsto \llbracket e \rrbracket_s]) \\ \langle x := \mathbf{new} \ e, \pi^s(s), \pi^h(h) \rangle &\rightsquigarrow (\pi^s(s) | x \mapsto l_\pi), (\pi^h(h) * [l_\pi \mapsto \llbracket e \rrbracket_{\pi^s(s)}]) \\ \sigma' &= (s | x \mapsto l), (h * [l \mapsto \llbracket e \rrbracket_s]) \end{aligned}$$

Consider the original resulting heap looks as follows, with *l*(grey square) marking the minimal available(white square) location:



 $\sigma' = \pi' \bullet \sigma''$

And let's suppose the heap after applying π is as follows, with l_{π} marking the new minimal available location:

$$\sigma'' = (\pi^{s}(s)|x \mapsto l_{\pi}), (\pi^{h}(h) * [l_{\pi} \mapsto \llbracket e \rrbracket_{\pi^{s}(s)}])$$

Let π be such that original l after applying π was mapped **not** to the minimal free location, then if we just take $\pi' = \pi^{-1}$ and apply it to σ'' heap will be something like this, with *l* being not minimal:



which is surely wrong.

In that case we can repair π' by taking it to be $\pi' = \pi^{-1}[l_{\pi} := l; \pi(l) := l_{\pi}]$. That is the same π^{-1} , but with cells mapped uncorrectly swapped to fix the problem: l_{π} , the newly allocated cell, is mapped to l, the location which would be allocated in original not renamed heap, and $\pi(l)$, the location original would-be-allocated cell is renamed to is mapped to l_{π} .

3. lookup:

 $\begin{array}{l} \langle x := [e], s, h \rangle \rightsquigarrow (s|x \mapsto h(\llbracket e \rrbracket_s)), h \\ \langle x := [e], \pi^s(s), \pi^h(h) \rangle \rightsquigarrow (\pi^s(s)|x \mapsto \pi^h(h)(\llbracket e \rrbracket_{\pi^s(s)})), \pi^h(h) \\ \\ \sigma'' = (s|x \mapsto h(\llbracket e \rrbracket_s)), h \\ \sigma'' = (\pi^s(s)|x \mapsto \pi^h(h)(\llbracket e \rrbracket_{\pi^s(s)})), \pi^h(h) \\ \\ \text{Let } \pi' := \pi^{-1}, \text{then} \\ \pi' \bullet \sigma'' = \pi' \bullet (\pi \bullet \sigma) \qquad by \ def. \ of \ \sigma'' \\ \\ = (\pi' \circ \pi) \bullet \sigma \qquad by \ Lemma \ 2 \\ \\ = \pi \qquad by \ def. \ of \ Id \\ \\ = \sigma \qquad by \ Lemma \ 2 \end{array}$

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4. mutation:

$$\langle [e] := e', s, h \rangle \rightsquigarrow s, (h | \llbracket e \rrbracket_s \mapsto \llbracket e' \rrbracket_s)$$

$$\langle [e] := \pi \bullet e', \pi^s(s), \pi^h(h) \rangle \rightsquigarrow \pi^s(s), (\pi^h(h) | \llbracket e \rrbracket_{\pi^s(s)} \mapsto \llbracket e' \rrbracket_{\pi^s(s)})$$

$$\begin{aligned} \sigma' &= s, (h|\llbracket e \rrbracket_s \mapsto \llbracket e' \rrbracket_s) \\ \sigma'' &= \pi^s(s), (\pi^h(h) |\llbracket e \rrbracket_{\pi^s(s)} \mapsto \llbracket e' \rrbracket_{\pi^s(s)}) \end{aligned}$$
Let $\pi' := \pi^{-1}$, then
 $\pi' \bullet \sigma'' &= \pi' \bullet (\pi \bullet \sigma) \qquad by \ def. \ of \ \sigma'' \\ &= (\pi' \circ \pi) \bullet \sigma \qquad by \ Lemma \ 2 \\ &= (\pi^{-1} \circ \pi) \bullet \sigma \qquad by \ def. \ of \ \pi' \\ &= Id \bullet \sigma \qquad by \ def. \ of \ Id \\ &= \sigma \qquad by \ Lemma \ 2 \end{aligned}$

5. **dispose:** $\langle \text{dispose } e, s, h \rangle \rightsquigarrow s, h - \llbracket e \rrbracket_s$

 $\langle \mathbf{dispose} \ e, \pi^s(s), \pi^h(h) \rangle \rightsquigarrow \pi^s(s), \pi^h(h) - \llbracket e \rrbracket_{\pi^s(s)}$

$$\begin{aligned} \sigma' &= s, h - \llbracket e \rrbracket_s \\ \sigma'' &= \pi^s(s), \pi^h(h) - \llbracket e \rrbracket_{\pi^s(s)} \end{aligned} \\ \mbox{Let } \pi' &:= \pi^{-1}, \mbox{then} \\ \pi' &\bullet \sigma'' &= \pi' \bullet (\pi \bullet \sigma) \\ &= (\pi' \circ \pi) \bullet \sigma \\ &= (\pi^{-1} \circ \pi) \bullet \sigma \\ &= Id \bullet \sigma \\ &= \sigma \end{aligned} \qquad by \ def. \ of \ Id \\ &= \sigma \end{aligned}$$

6. assignment:

$$\begin{aligned} \langle x := e, s, h \rangle &\rightsquigarrow (s | x \mapsto \llbracket e \rrbracket_s), h \\ \langle x := e, \pi^s(s), \pi^h(h) \rangle &\rightsquigarrow (\pi^s(s) | x \mapsto \llbracket e \rrbracket_{\pi^s(s)}), \pi^h(h) \end{aligned}$$

$$\begin{aligned} \sigma' &= (s|x \mapsto \llbracket e \rrbracket_s), h \\ \sigma'' &= (\pi^s(s)|x \mapsto \llbracket e \rrbracket_{\pi^s(s)}), \pi^h(h) \end{aligned}$$
Let $\pi' := \pi^{-1}$, then
 $\pi' \bullet \sigma'' &= \pi' \bullet (\pi \bullet \sigma) \qquad by \ def. \ of \ \sigma'' \\ &= (\pi' \circ \pi) \bullet \sigma \qquad by \ Lemma \ 2 \\ &= (\pi^{-1} \circ \pi) \bullet \sigma \qquad by \ def. \ of \ \pi' \\ &= Id \bullet \sigma \qquad by \ def. \ of \ Id \\ &= \sigma \qquad by \ Lemma \ 2 \end{aligned}$

At this point we have proven all the base cases.

7. conditional:

 $\langle \text{if } b \text{ then } c_0 \text{ else } c_1, s, h \rangle \rightsquigarrow s', h'$ $\langle \text{if } b \text{ then } c_0 \text{ else } c_1, \pi^s(s), \pi^h(h) \rangle \rightsquigarrow s'', h''$ $\begin{aligned} \sigma' &= s', h' \\ \sigma'' &= s'', h'' \end{aligned}$

Depending on *b*, either c_0 or c_1 is executed:

- case *b* true $\langle c_0, s, h \rangle \rightsquigarrow s', h'$ $\langle c_0, \pi^s(s), \pi^h(h) \rangle \rightsquigarrow s'', h''$ By induction hypothesis we can conclude that π' exists.
- case *b false* analogously.

while, sequencing are proved the same way.

Part2.

 $\langle c, \sigma \rangle \rightsquigarrow \text{abort} \Rightarrow \langle c, \pi \bullet \sigma \rangle \rightsquigarrow \text{abort}$

8. **abort** cases:

abort arises when command tries to access inactive memory ($[e]_s \notin dom(h)$). To show

$$(c, \pi^s(s), \pi^h(h)) \rightsquigarrow abort$$

it suffices to show: $\llbracket e \rrbracket_{\pi^s(s)} \notin dom(\pi^h(h))$ whenever $\llbracket e \rrbracket_s \notin dom(h)$

Suppose $\llbracket e \rrbracket_s \notin dom(h)$ and $\llbracket e \rrbracket_{\pi^s(s)} \in dom(\pi^h(h))$, then

- (a) $\llbracket e \rrbracket_{\pi^s(s)} = \pi(l)$, for some $l \in dom(h)$, then by Lemma 3.3
- (b) $\pi^{-1 \ val}(\llbracket e \rrbracket_{\pi^{s}(s)}) = \pi^{-1 \ val}(\pi^{val}(l)) = l$ by Lemma 3.1 $l = \llbracket e \rrbracket_{s}$. contradiction Therefore $\llbracket e \rrbracket_{\pi^{s}(s)} \notin dom(\pi^{h}(h))$

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Chapter 4

Logic

In this section we will provide the separation logic for our language with deterministic semantics. We change almost nothing here compared to Reynolds. Separation logic contains all of the boolean expressions, classical logic plus adds special assertions to describe the heap. For the purposes of checking the type correctness we added another two predicates, testing whether expression is boolean or is defined.

Hoare triples specifying the program will be interpreted according to the partial correctness.

4.1 Assertions

Assertions of our logic are usual formulae of separation logic(without separating implication):

$p,q,r::=b \mid p \Rightarrow q \mid$	$\forall x.p$
emp	empty heap
$ e \mapsto f$	singleton heap
p * q	separating conjunction
<i>isbool</i> (<i>e</i>)	e is boolean
isdef(e)	e is defined

Table 4.1: Assertions

Meaning of an assertion depends on both store and heap. When an assertion p is **true** in some state σ we say that *the state satisfies the assertion* and write:

$$\sigma \models p$$

Abbreviations are as usual: $e_0 = e_1$: $e_0 \le e_1 \land e_1 \le e_0$; $\exists x.p : \neg \forall x.\neg p$; $x \mapsto - : \exists v.x \mapsto v$.

The meaning of heap assertions:

• *isbool*(*e*) is **true** only when *e* is boolean:

 $\sigma \models isbool(e) \Leftrightarrow \llbracket e \rrbracket_s \in boolean$

• *isdef*(*e*) is **true** only when *e* is defined:

$$\sigma \models isdef(e) \Leftrightarrow \llbracket e \rrbracket_s \neq undefined$$

• **emp** is **true** only when the current heap is empty:

$$\sigma \models \mathbf{emp} \Leftrightarrow dom(h) = \oslash$$

e → *f* is **true** only when the current heap contains one element at the location *e* with the value *f*:

$$\sigma \models e \mapsto f \Leftrightarrow dom(h) = \{\llbracket e \rrbracket_s\} \text{ and } h(\llbracket e \rrbracket_s) = \llbracket e' \rrbracket_s$$

• **p** * **q** is **true** only when the current heap can be split into two disjoint parts and for each of them *p* and *q* hold respectively:

$$s,h \models p * q \Leftrightarrow \exists h_0, h_1$$
. $h_0 \perp h_1$ and $h_0 \cdot h_1 = h$ and $s, h_0 \models p$ and $s, h_1 \models q$

Two heaps are disjoint if they have disjoint domains:

$$dom(h_0) \cap dom(h_1) = \oslash$$

We write $h_0 \perp h_1$ to show that h_0 is disjoint from h_1 , and $h_0 \cdot h_1$ to denote the heap combined from these disjoint parts.

4.2 Partial Correctness

Assertions and commands are combined into Hoare triples to reason about programs:

 ${p}c{q}$

p is a *precondition*, q is a *postcondition*, c is a command. A triple describes how the execution of a command changes the state of the program, from state satisfying the

precondition to the state satisfying the postcondition. In our work we are going to consider *partial correctness* interpretation of Hoare triples, i.e. we don't take into account requirement for the command to necessarily terminate, which would give us a *total correctness*.

We say that the command is *safe*, if it does not abort: $\langle c, \sigma \rangle \not\rightarrow \text{abort}$, and that Hoare triple is true $\models \{p\}c\{q\}$, iff in all states satisfying precondition executing command is safe and the resulting state satisfies the postcondition:

$$\models \{p\}c\{q\} \Leftrightarrow \forall \sigma. \quad \sigma \models p \Rightarrow \langle c, \sigma \rangle \text{ is safe, and} \\ \langle c, \sigma \rangle \rightsquigarrow \sigma' \Rightarrow \sigma' \models q$$

4.3 Inference rules

Inference rules for Hoare triples are given in Table 4.2.

skip	assign
$\overline{\{p\}\mathbf{skip}\{p\}}$	$\overline{\{p[e/x]\}x := e\{p\}}$
seq	cond
${p}c_0{t} = {t}c_1{q}$	$\{p \land b\}c_0\{q\}$ $\{p \land \neg b\}c_1\{q\}$
${p}c_0; c_1{q}$	$\{p \land isbool(b)\}$ if b then c_0 else $c_1\{q\}$
while	weak
$\{p \land b\}c\{p\}$	$p \Rightarrow s \qquad \{s\}c\{t\} \qquad t \Rightarrow q$
$\overline{\{p\}}$ while \land isbool(b) b do $c\{\neg b \land p\}$	${p}c{q}$
alloc	mutate
$\overline{\{\mathbf{emp} \land isdef(e)\}x := \mathbf{new} \ e\{x \mapsto e\}}$	$\overline{\{e\mapsto -\}[e]:=e'\{e\mapsto e'\}}$
lookup	dispose
$\overline{\{x = y \land (e \mapsto z)\}x := [e]\{x = z \land (e[x := y] \mapsto z)\}}$	$\{e \mapsto -\}$ dispose $e\{emp\}$
frame	
$\frac{\{p\}c\{q\}}{\{p*r\}c\{q*r\}} , mod$	$l(c) \cap fv(r) = \{\}$

Table 4.2: Inference rules

Rules remain the same as in separation logic. *Skip* doesn't change anything. The *assignment* axiom states that after the assignment any predicate holds for the variable that was previously true for the right-hand side of the assignment. p[e/x] denotes the assertion p in which all free occurrences of the variable x have been replaced with the expression e. As an example:

$$\{y+1 = 42 \land y = 5\}x := y+1\{0 \le x\}$$

Sequencing allows composing commands if the postcondition of the first one mathes precondition of the second. *Conditional* allows executing one of two commands depending on some condition. *While* loops a command depending on condition. *Weakening* strengthens the precondition and weakens the postcondition.

Allocation: starting in a state with empty heap execution ends in a state with single cell allocated. *Mutation*: starting in a state where heap contains a single active cell and store with a variable pointing to this memory cell changes the content of this cell. *Lookup*: assigns to the variable *x* the content of the heap cell specified by expression *e*. *Dispose*: starting in a state with single cell allocated and pointed to by variable, ends in a state with an empty heap.

The most important is the Frame rule. It has as a side condition the requirement for free variables of r, fv(r), not to be modified by c, mod(c) is the set of variables updated by c. The rule is the key to *local reasoning* about the heap. Local reasoning reflects the informal intuition of programmers who usually concentrate only on relevant resources used by part of the program:

To understand how a program works, it should be possible for reasoning and specification to be confined to the cells that the program actually accesses. The value of any other cell will automatically remain unchanged.[8]

Frame rule allows deriving global versions of the previously described rules, e.g. from local mutation:

$$\overline{\{x\mapsto -\}[x] := e\{x\mapsto e\}}$$

to global:

$$\overline{\{(x\mapsto -)*r\}[x] := e\{(x\mapsto e)*r\}}$$

Lemma 4.1

 $\forall p.\forall s,s', h. s(x) = s'(x) \text{ for all } fv(p) \Rightarrow ((s,h) \models p \Rightarrow (s',h) \models p)$

Proof by induction on *p*.

Lemma 4.2

 $\forall p. \forall s, h, \pi. \ (s, h) \models p \Rightarrow (\pi^s(s), \pi^h(h)) \models p$

Proof by induction on *p*.

Chapter 5

Soundness

Soundness of our logic is proved by rule induction. Proof will be split into three parts: Hoare rules, Heap rules and the most important - frame rule.

1. Traditional Hoare rules

(a) **skip** To show: $\models \{p\}$ **skip** $\{p\}$

> 1)Assume $\sigma \models p$ 2)By rule for skip: $\langle \mathbf{skip}, \sigma \rangle \rightsquigarrow \sigma$

 $\Rightarrow \models \{p\} \mathbf{skip}\{p\}$

(b) assignment

To show: $\models \{p[e/x]\}x := e\{p\}$

1)Assume $s, h \models p[e/x]$ 2)By rule for assignment: $\langle x := e, s, h \rangle \rightsquigarrow s \mid x \mapsto \llbracket e \rrbracket_s, h$ 3)Using lemma 6.9 from [15] $s \mid x \mapsto \llbracket e \rrbracket_s, h \models p$

 $\Rightarrow \models \{p[e/x]\}x := e\{p\}$

(c) sequencing

To show: $\frac{\models \{p\}c_0\{t\}}{\models \{p\}c_0; c_1\{q\}}$

1) Assume
$$\models \{p\}c_0\{t\}; \models \{t\}c_1\{q\}$$

2) Suppose $\sigma \models p$
3) Then $\langle c_0, \sigma \rangle \rightsquigarrow \sigma'$ and from 1) $\sigma' \models t$
4) Then $\langle c_1, t \rangle \rightsquigarrow \sigma''$ and from 1) $\Rightarrow \sigma'' \models q$
 $\Rightarrow \frac{\models \{p\}c_0\{t\} \models \{t\}c_1\{q\}}{\models \{p\}c_0; c_1\{q\}}$

(d) conditional
To show:
$$\frac{\models \{p \land b\}c_0\{q\}}{\models \{p \land isbool(b)\} \text{if } b \text{ then } c_0 \text{ else } c_1\{q\}}$$

1) Assume $\models \{p \land b\}c_0\{q\}; \models \{p \land \neg b\}c_1\{q\}$ 2) Suppose $\sigma \models p \land isbool(b)$ then 3) if $\sigma \models b$ then 1) and $\langle c_0, \sigma \rangle \rightsquigarrow \sigma' \models q$ or if $\sigma \models \neg b$ then 1) and $\langle c_1, \sigma \rangle \rightsquigarrow \sigma'' \models q$

$$\Rightarrow \frac{\models \{p \land b\}c_0\{q\}}{\models \{p\} \text{ if } b \text{ then } c_0 \text{ else } c_1\{q\}}$$

(e) while-loop

To show:
$$\frac{\models \{p \land b\}c\{p\}}{\models \{p \land isbool(b)\} \text{while } b \text{ do } c\{\neg b \land p\}}$$

1) Assume $\models \{p \land b\}c\{p\}$ 2) Suppose $\sigma \models p$ case $\sigma \models \neg b$:

(while *b* do *c*, σ) $\rightsquigarrow \sigma$

$$\sigma \models p \land \neg b$$

case $\sigma \models b$:

 $\langle c, \sigma \rangle \rightsquigarrow \sigma_3$; (while *b* do $c, \sigma_3 \rangle \rightsquigarrow \sigma_2$

3) 1)
$$\Rightarrow \sigma_3 \models p$$

4)By induction $\sigma_2 \models p \land \neg b$
 $\Rightarrow \frac{\models \{p \land b\}c\{p\}}{\models \{p\}$ while *b* do $c\{\neg b \land p\}$

To show: $\frac{\models p \Rightarrow s \qquad \models \{s\}c\{t\} \qquad \models t \Rightarrow q}{\models \{p\}c\{q\}}$ 1) Assume $\models p \Rightarrow s; \models \{s\}c\{t\}; \models t \Rightarrow q$ 2) Suppose $\sigma \models p$ 3) From 1) and 2): $\sigma \models s$ 4) From 1) and 3): $\Rightarrow \langle c, \sigma \rangle \rightsquigarrow \sigma' \models t$ 5) From 1): $\Rightarrow \sigma' \models q$ $\Rightarrow \frac{\models p \Rightarrow s \qquad \models \{s\}c\{t\} \qquad \models t \Rightarrow q}{\models \{p\}c\{q\}}$

2. Heap rules

(a) allocation

To show: $\models \{\mathbf{emp} \land isdef(e)\}x := \mathbf{new} \ e\{x \mapsto e\}$

Suppose $s, h \models emp \land isdef(e)$ then $\langle x := new \ e, s, h \rangle \rightsquigarrow \langle s \mid x : l, l \rightarrow [[e]]_s \rangle$ and

 $\langle s \mid x: l, l \to \llbracket e \rrbracket_s \rangle \models x \mapsto e$

(b) mutation

To show: $\models \{x \mapsto -\land isdef(e)\}[x] := e\{x \mapsto e\}$

Suppose $s, h \models x \mapsto - \land isdef(e)$ then $\langle [x] := e, s, h \rangle \rightsquigarrow \langle s, [\![x]\!]_s \to [\![e]\!]_s \rangle$ and

 $\langle s, \llbracket x \rrbracket_s \to \llbracket e \rrbracket_s \rangle \models x \mapsto e$

(c) lookup

To show: $\models \{x = y \land (e \mapsto z) \land isdef(e)\}x := [e]\{x = z \land (e' \mapsto z)\}, e' = e[x := y]$

Lemma 5.1 $[\![e[x := y]]\!]_s = [\![e]\!]_{s[x:=s(y)]}$

Suppose $s, h \models x = y \land (e \mapsto z)$ then $\langle x := [e], s, h \rangle \rightsquigarrow \langle s | x \rightarrow h(\llbracket e \rrbracket_s), h \rangle$ and

$$\langle s | x \to h(\llbracket e \rrbracket_s), h \rangle \models x = z \land (e' \mapsto z)$$
 by Lemma 5.1

(d) deallocation To show: $\models \{e \mapsto -\}$ dispose $e\{emp\}$

Suppose $s, h \models e \mapsto$ then $\langle \text{dispose } e, s, h \rangle \rightsquigarrow (s, \text{emp})$ and

 $(s, emp) \models emp$

3. Frame rule

To show: $\frac{\models \{p\}c\{q\}}{\models \{p * r\}c\{q * r\}}$, $mod(c) \cap fv(r) = \{\}$

Extension of permutation theorem

In order to prove the frame rule we will rephrase semantic property of safety monotonicity and the Frame property from [17] according to permutation theorem.

Lemma 5.2 (Safety monotonicity)

• If $\langle c, s, h \rangle$ safe, and π -renaming and $(\pi^h(h)) \perp h'$, then $\langle c, \pi^s(s), \pi^h(h) \cdot h' \rangle$ is safe

Proof: Safety monotonicity

Proof is similar to Part 2 of permutation theorem. Suppose $\langle c, s, h \rangle$ safe and $\langle c, \pi^s(s), \pi^h(h) \cdot h' \rangle$ isn't. Then $\llbracket e \rrbracket_{\pi^s(s)} \notin dom(\pi^h(h) \cup h)$ dom(h')), and therefore also $\llbracket e \rrbracket_{\pi^s(s)} \notin dom(\pi^h(h))$.

(a) $\llbracket e \rrbracket_{\pi^s(s)} = \pi(l)$, for some $l \in dom(h)$, then by Lemma 3.3 (b) $\pi^{-1 \ val}(\llbracket e \rrbracket_{\pi^s(s)}) = \pi^{-1 \ val}(\pi^{val}(l)) = l$ by Lemma 3.1 $l = \llbracket e \rrbracket_s.$ contradiction

Contradiction.

Lemma 5.3 (Frame property) .

c is safe for
$$\sigma = (s, h_0)$$
 and $\langle c, s, h_0 \cdot h_1 \rangle \rightsquigarrow (s', h') \Rightarrow$
 $\exists \pi, h'_0. \langle c, s, h_0 \rangle \rightsquigarrow (s'', h'_0)$ and $h' = (\pi^h(h'_0)) \cdot h_1, s' = \pi^s(s'')$

Proof by structural induction:

(a) allocation $\langle x := \mathbf{new} \ e, s, h_0 \rangle \rightsquigarrow \langle (s \mid x \mapsto l_0), (h_0 \cdot l_0 \mapsto \llbracket e \rrbracket_s)) \rangle, l_0 \text{ - minimal}$ $\overbrace{I_0}^{h'_0}$ $\langle x := \mathbf{new} \ e, s, h_0 \cdot h' \rangle \rightsquigarrow \langle (s \mid x \mapsto l'_0), (h_0 \cdot h_1 \cdot l'_0 \mapsto \llbracket e \rrbracket_s)) \rangle, l'_0 \text{ - minimal}$ $\stackrel{h'}{\blacksquare}$

$$\pi = id \mid l_0 \mapsto l'_0, h'_0 = h_0 \cdot l'_0 \mapsto \llbracket e \rrbracket_s \qquad \square$$

(b) *mutation*:
$$\langle [x] := e, s, h_0 \rangle \rightsquigarrow \langle s, (h_0 \mid \llbracket x \rrbracket_s \mapsto \llbracket e \rrbracket_s) \rangle$$
$$\langle [x] := e, s, h_0 \cdot h_1 \rangle \rightsquigarrow \langle s, (h_0 \cdot h_1 \mid \llbracket x \rrbracket_s \mapsto \llbracket e \rrbracket_s) \rangle$$

$$\pi = id, h'_0 = h_0 \mid \llbracket x \rrbracket_s \mapsto \llbracket e \rrbracket_s$$

(c)	lookup:
	$\langle x := [e], s, h_0 \rangle \rightsquigarrow \langle (s \mid x \mapsto h(\llbracket e \rrbracket_s)), h_0 \rangle$
	$\langle x := [e], s, h_0 \cdot h_1 \rangle \rightsquigarrow \langle (s \mid x \mapsto h(\llbracket e \rrbracket_s)), h_0 \cdot h_1 \rangle$

$$= id, h'_0 = h_0$$

(d) dispose: $\langle \text{dispose } e, s, h_0 \cdot \llbracket e \rrbracket_s \mapsto - \rangle \rightsquigarrow \langle s, h_0 \rangle$ $\langle \text{dispose } e, s, h_0 \cdot \llbracket e \rrbracket_s \mapsto - \cdot h_1 \rangle \rightsquigarrow \langle s, h_0 \cdot h_1 \rangle$

$$\pi = id, h'_0 = h_0$$

 π

Cases for **skip**, and *assignment* are similar to the last two rules, since they don't modify the heap.

 \Box Base cases are proven. Cases for *conditional, while* and *sequencing* are by induction.

Now back to the Frame rule:

Need to prove: $\frac{\models \{p\}c\{q\}}{\models \{p*r\}c\{q*r\}} \qquad , mod(c) \cap fv(r) = \{\}$

Suppose $s, h \models p * r$, i.e.:

 $(s, h_0) \models p$ and $(s, h_1) \models r$ To show:

- (a) $\langle c, s, h \rangle$ safe,
- (b) $\langle c, s, h \rangle \rightsquigarrow \langle s', h' \rangle \Rightarrow s', h' \models q * r$,

Safety of $\langle c, s, h \rangle$ follows from safety monotonicity.

If $\langle c, (s, h_0 \cdot h_1) \rangle \rightsquigarrow (s', h')$, then $\exists \pi, h'_0, \langle c, (s, h_0) \rangle \rightsquigarrow \langle s'', h'_0 \rangle$

$$\wedge h' = (\pi \bullet h'_0) \cdot h_1, \quad s' = \pi \bullet s''$$
 by Lemma 5.3

$$\Rightarrow$$
 (*s*', *h*₀') \models *q* by assumption \models {*p*}*c*{*q*}

- $\Rightarrow (\pi \bullet s'', \pi \bullet h'_0) \models q$ by Lemma 4.2
- $\Rightarrow (\pi \bullet s', h') \models q * r$ by Lemma 4.1 and $mod(c) \cap fv(r) = \{\}$

Chapter 6

Conclusion and Future Work

6.1 Conclusion

In this work we have followed the direction stated by Yang/O'Hearn in [17] to walk around the non-determinancy in allocation. We confirmed that allocation is not necessarily should be non-deterministic, if we have a language without address arithmetic and invariance under location renaming. The central contribution of this work is a formal statement and proof of the permutation theorem. This proof with the given deterministic semantics shows that running a program on a correspondingly renamed heap leads to the same result, up to a renaming of locations in the final heap. We have shown that it is always possible to find the needed permutation, and in most cases it was simply the inverse of original permutation. The only problem was with allocation, since renaming could map the original minimal location to arbitrary cell, not necessarily being also minimal, and we have shown that suitable permutation is found in this case by "swapping" uncorrectly mapped locations. Next we gave the separation logic for our language and proved its soundness.

6.2 Future Work

As usual there are a lot of open ends to explore. Traditionally allocation was treated non-deterministically and so far most of works were done based on this approach. Now it would be interesting to apply what is currently done to our approach, like extending it to more expressive languages e.g. with procedures or shared-variable concurrency, or proving completeness of this logic. We also thought about deterministic allocation for more than one cell and found that it is still possible to find suitable permutation function, but the problem is that permutation does not preserve consequetive memory blocks, and this is left for future. Reynolds in [12] said that there is a hope to construct a garbage collector in a situation where addresses are disjoint from integers, which is ours, although we didn't think about this yet, since allocation always chooses a minimal address we have an implicit memory reuse.

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