

# Semantics of an Intermediate Language for Program Transformation

Sigurd Schneider  
Advisors: Sebastian Hack, Gert Smolka

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Saarland University  
Graduate School of Computer Science

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- 1 Compiler technology is critical for
  - ▶ Performance
  - ▶ Reliability
  - ▶ Availability
- 2 State of the art
  - ▶ Unverified: LLVM, HotSpot, GCC, MSVC, ICC
  - ▶ Verified: CompCert
  - ▶ As of today, compiler correctness is hard
- 3 How to verify advanced optimizations in an extensible way?
  - ▶ Optimizations from research compiler
  - ▶ Correctness proofs in type theory (Coq)

## 1 Functional intermediate language

- ▶ Well-understood semantics
- ▶ Compiler more effective and easier to implement

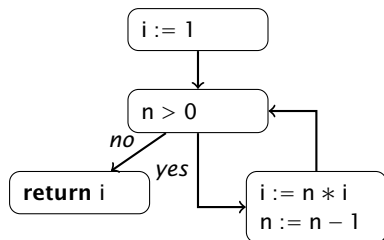
## 2 State of the art in functional IRs

- ▶ VSDG: Higher Order [Johnson and Mycroft 2003]
- ▶ PEG: “Stream Semantics” [Tate et al. 2009]
- ▶ Firm-IR: “Click’s Sea of Nodes” [Braun, Buchwald, and Zwinkau 2011]
- ▶ CompCert does not use functional intermediate language

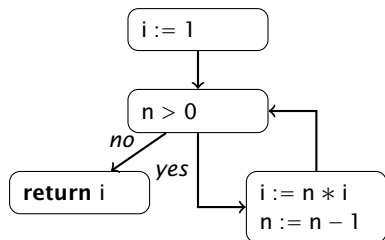
## 3 Research questions

- ▶ Are proofs simpler and more modular for functional IL?
- ▶ Can we exploit results from the literature?
- ▶ **Translation: Imperative  $\Leftrightarrow$  Functional**

# IL/I: Formalizing Control Flow Graphs



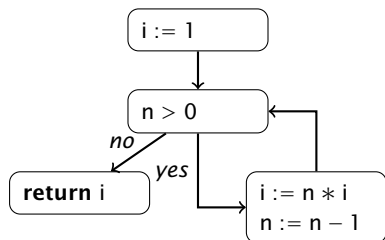
# IL/I: Formalizing Control Flow Graphs



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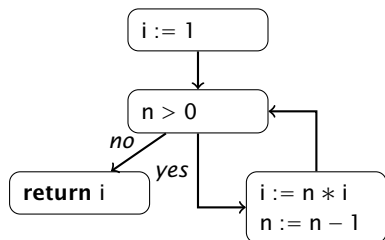
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- CFG can be **represented** by IL/I
  - 1 (Irreducible) control flow as (mutual) recursion
  - 2 Data flow through imperative variables
- Parameter passing is parallel assignment



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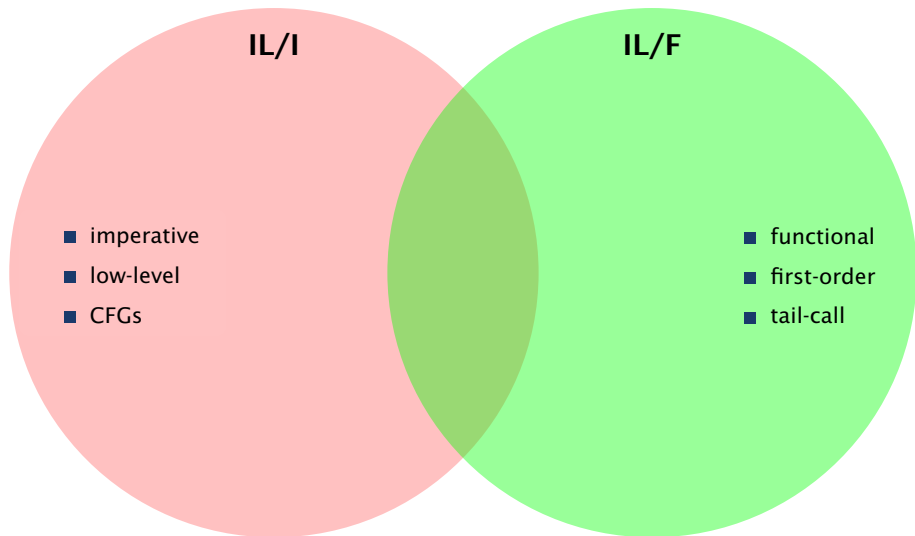
- CFG can be **translated** to functional language IL/F
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  - 2 Data flow through binding and explicit arguments
- Static Single Assignment (SSA) [Appel 1998; Kelsey 1995]  
[Barthe, Demange, and Pichardie 2012; Zhao and Zdanczewicz 2012]



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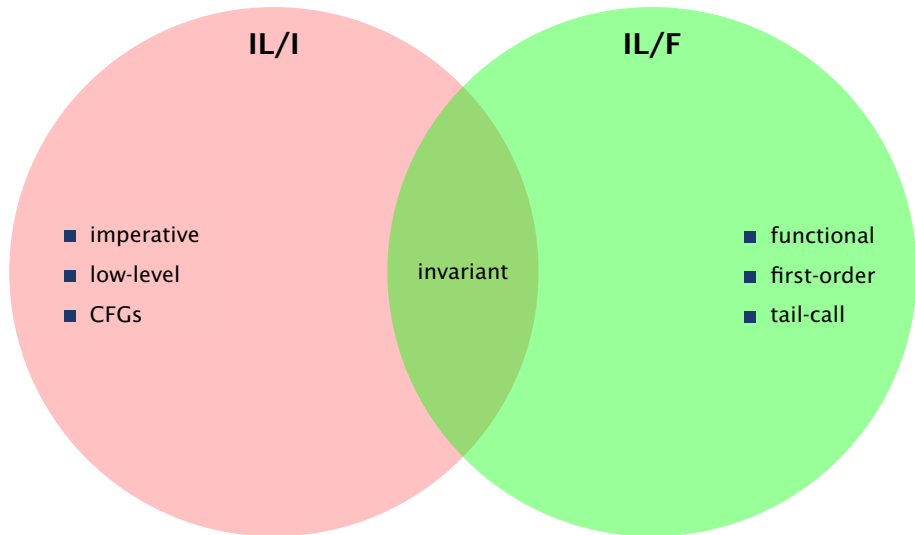
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# IL: One syntax, two semantic interpretations

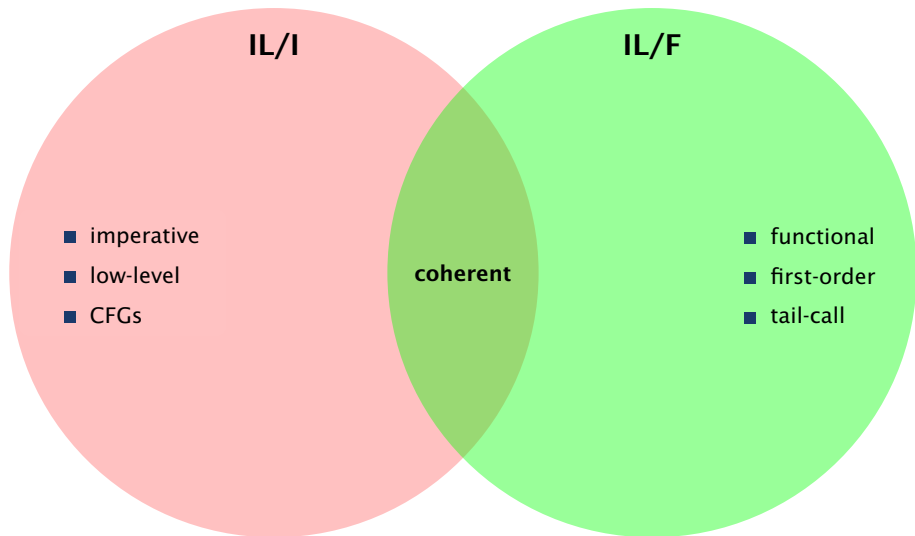




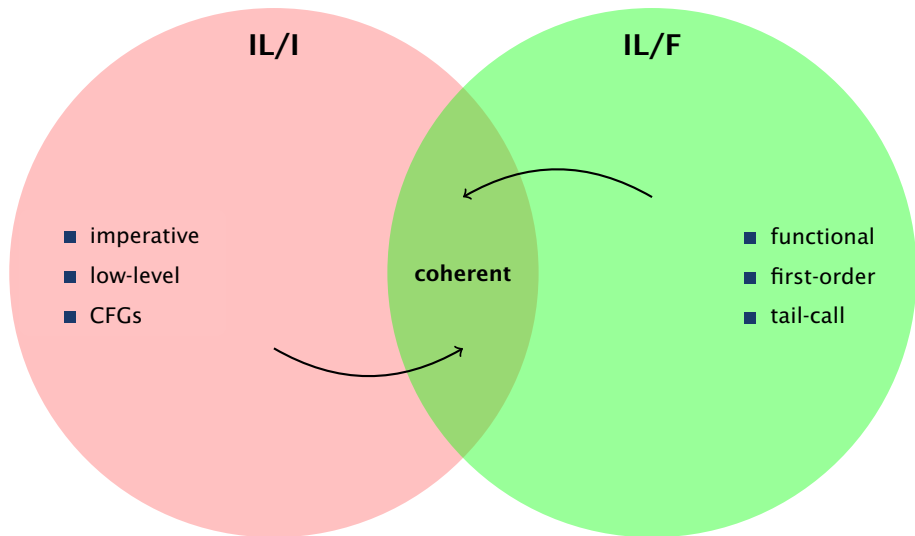
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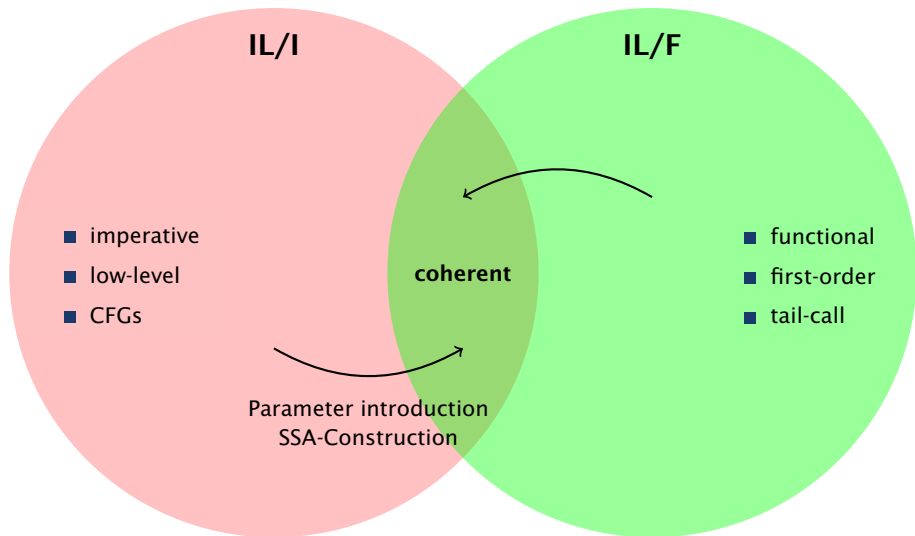
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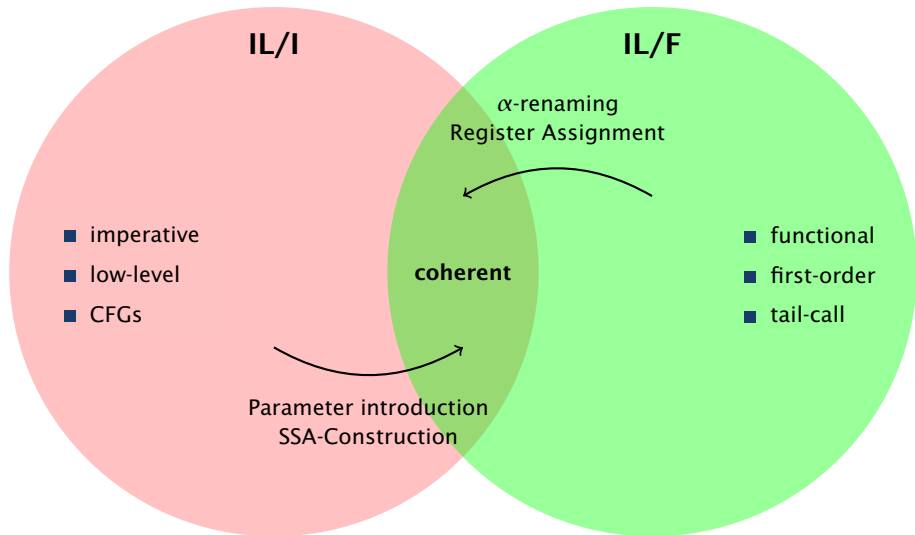
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# Register Allocation: Example

- Register assignment
  - $\alpha$ -renaming to coherent program
- Parameter elimination
  - parallel assignment [Rideau, Serpette, and Leroy 2008]

4 registers in use: i,j,m,n

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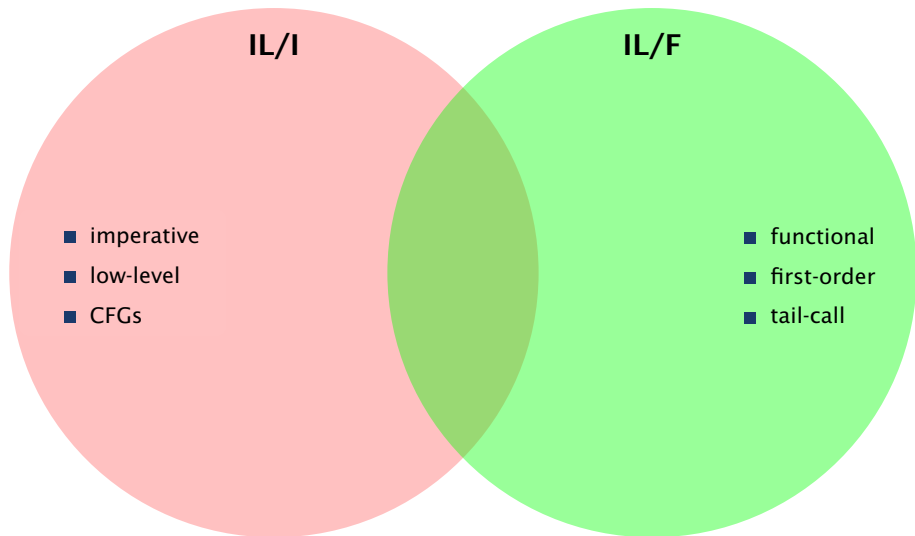
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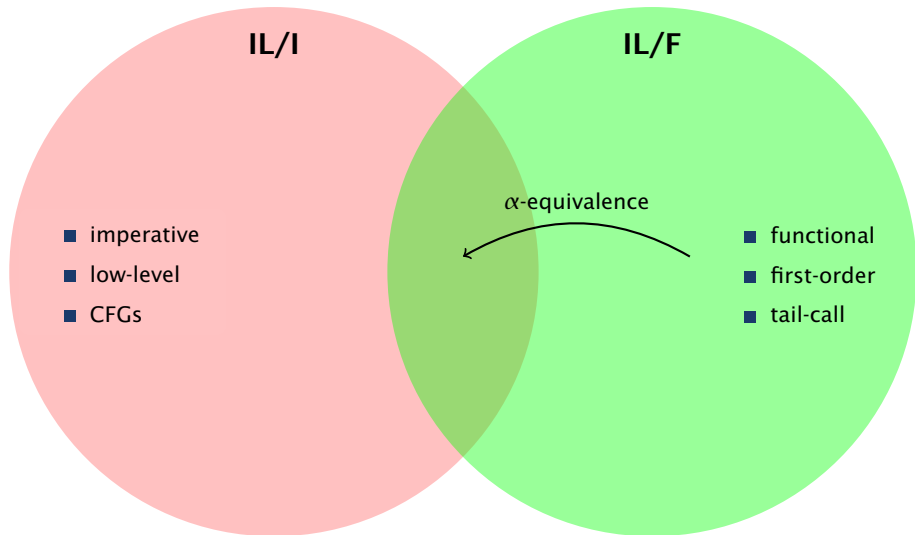
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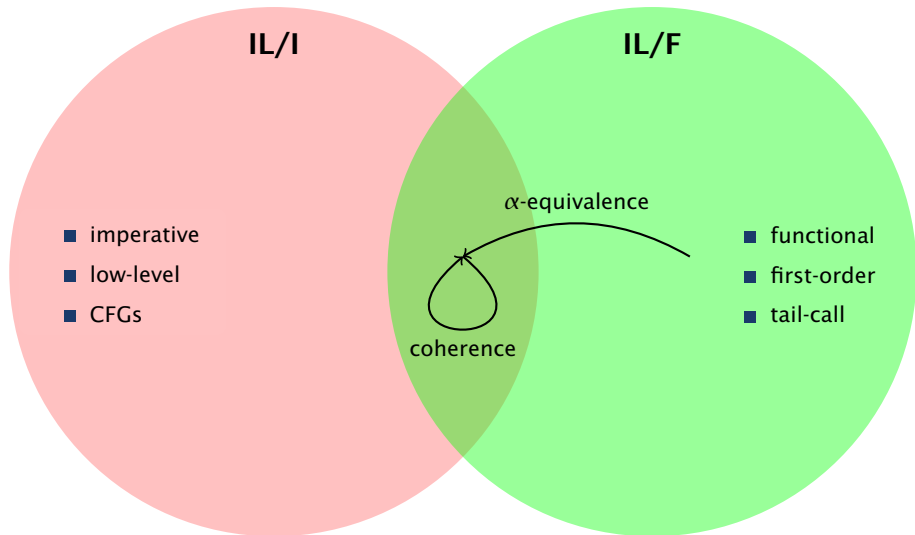
# Register Allocation: Correctness Argument



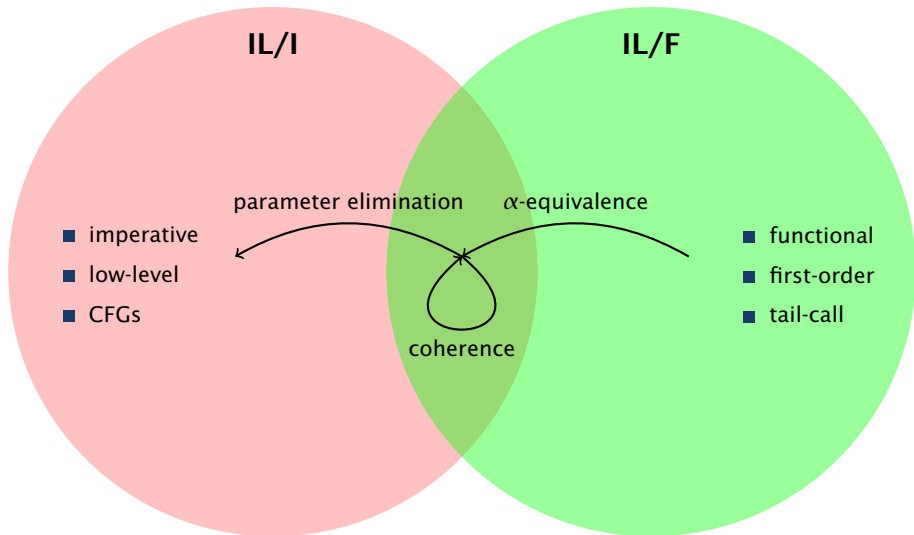
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# Register Allocation: Correctness Argument



- 1 IL provides bridge between imperative and functional world
- 2 Correctness arguments on functional side
  - ▶ register allocation is  $\alpha$ -renaming to coherent program
- 3 Formalization in Coq
  
- 4 Future work
  - ▶ Application of aggressive optimizations on the functional side [Chakravarty, Keller, and Zadarnowski 2003]
  - ▶ Contextual program equivalence in IL

# Thank you for listening! Questions? I

Thanks for the great questions and discussion after the Talk, pointing out most closely related work by Beringer, MacKenzie, and Stark (2003), who also present a language with a dual semantic interpretation.

Appel, Andrew W. (1998). “SSA is Functional Programming”. In: *SIGPLAN Notices* 4.

Barthe, Gilles, Delphine Demange, and David Pichardie (2012). “A Formally Verified SSA-Based Middle-End - Static Single Assignment Meets CompCert”. In: *ESOP*. Ed. by Helmut Seidl. Lecture Notes in Computer Science.

Beringer, Lennart, Kenneth MacKenzie, and Ian Stark (2003). “Grail: a functional form for imperative mobile code”. In: *Electr. Notes Theor. Comput. Sci.* 1.

Braun, Matthias, Sebastian Buchwald, and Andreas Zwinkau (2011). *Firm—A Graph-Based Intermediate Representation*. Tech. rep. 35. Karlsruhe Institute of Technology.

Chakravarty, Manuel M. T., Gabriele Keller, and Patryk Zadarnowski (2003). “A Functional Perspective on SSA Optimisation Algorithms”. In: *Electr. Notes Theor. Comput. Sci.* 2.

Click, Cliff and Michael Paleczny (1995). “A Simple Graph-Based Intermediate Representation”. In: *Intermediate Representations Workshop*. Ed. by Michael D. Ernst.

Johnson, Neil and Alan Mycroft (2003). “Combined Code Motion and Register Allocation Using the Value State Dependence Graph”. In: *CC*. Ed. by Zhong Shao and Benjamin C. Pierce.

Kelsey, Richard A. (Mar. 1995). “A correspondence between continuation passing style and static single assignment form”. In: *SIGPLAN Not.* (3). ISSN: 0362-1340.

# Thank you for listening! Questions? II

Leroy, Xavier (2009). “Formal verification of a realistic compiler”. In: *Communications of the ACM* 7.

Rideau, Laurence, Bernard Paul Serpette, and Xavier Leroy (2008). “Tilting at windmills with Coq: Formal verification of a compilation algorithm for parallel moves”. In: *Journal of Automated Reasoning* 4.

Tate, Ross et al. (2009). “Equality saturation: a new approach to optimization”. In: *POPL*. Ed. by Zhong Shao and Benjamin C. Pierce.

Zhao, Jianzhou and Steve Zdancewic (2012). “Mechanized Verification of Computing Dominators for Formalizing Compilers”. In: *CPP*. Ed. by Chris Hawblitzel and Dale Miller. Lecture Notes in Computer Science.



# Example: Parameter Elimination

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Figure : Semantics of IL/F, standard presentation

$$\text{Std-Op} \frac{V \vdash e \Downarrow v}{L, V, \text{let } x = e \text{ in } s \rightarrow L, V_v^x, s}$$

$$\text{Std-If} \frac{\text{val2bool}(Vx) = i}{L, V, \text{if } x \text{ then } s_0 \text{ else } s_1 \rightarrow L, V, s_i}$$

$$\text{Std-Let} \frac{c = (L, V, s, \bar{x})}{L, V, \text{fun } f \bar{x} = s \text{ in } t \rightarrow L_c^f, V, t}$$

$$\text{Std-App} \frac{Lf = (L', V', \bar{x}, s)}{L, V, f \bar{y} \rightarrow L_{Lf}^f, V'_{V\bar{y}}, \bar{x}, s}$$